

Alt  
connected to the fourth diode D4 defines a second connection  
of the load-dependent DC voltage source.--

In the Claims:

A2  
Claim 1 (amended). A circuit configuration, comprising:

an AC voltage input terminal and an AC voltage output  
terminal;

a plurality of frequency domain filter paths defined between  
said AC voltage input terminal and said AC voltage output  
terminal, and connected in parallel between a common first  
node and a common second node both coupled to a DC voltage  
connection;

each of said frequency domain filter paths containing at  
least one bandpass filter [according to claim 1] connected  
in series with a first diode and a second diode connected in  
opposite forward direction from said first diode;

said at least one bandpass filter including:

a bandpass filter input and a bandpass filter output;

a series circuit connected between said bandpass filter  
input and said bandpass filter output, said series  
circuit being formed of a first capacitor, a first

*As Cont*

parallel LC element connected to said first capacitor,  
a second capacitor connected to said first parallel LC  
element, and an inductor connected to said second  
capacitor;

a second parallel LC element having a first connection  
connected to a node between said first parallel LC  
element and said second capacitor and a second  
connection coupled to a fixed reference-ground  
potential via a third capacitor; and

a third parallel LC element having a first connection  
connected to a node between said second capacitor and  
said inductor and a second connection coupled to the  
fixed reference-ground potential;

each of said frequency domain filter paths containing a  
switching unit for switching said first and said second  
diode in said frequency domain filter path [during an  
operation of the circuit configuration for turning a  
respective one of said bandpass filters];

a third diode having a first terminal connected to said  
first node and a fourth diode having a first terminal  
connected to said second node of said frequency domain  
filter paths, such that a respective cathode of said third